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UTILITY PATENT APPLICATION TRANSMITTAL (Only for new nonprovisional applications under 37 CFR 1.63(b))	Attorney Docket No.	000265	Total Pages	1
	First Named Inventor or Application Identifier			
	L. Richard Carley			
	Express Mail Label No.	EL637983065US		

APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents.</small>	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
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2. <input checked="" type="checkbox"/> Specification [Total Pages 12] (preferred arrangement set forth below) <ul style="list-style-type: none">- Descriptive title of the invention- Cross References to Related Applications- Statement Regarding Fed sponsored R & D- Reference to Microfiche Appendix- Background of the invention- Brief Summary of the invention- Brief Description of the Drawings (if filed)- Detailed Description- Claim(s)- Abstract of the Disclosure	7. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) <ul style="list-style-type: none">a. <input type="checkbox"/> Computer Readable Copyb. <input type="checkbox"/> Paper Copy (identical to computer copy)c. <input type="checkbox"/> Statement verifying identity of above copies
3. <input checked="" type="checkbox"/> Drawing(s) (35 USC 113) [Total Sheets 10]	ACCOMPANYING APPLICATION PARTS <ul style="list-style-type: none">8. <input type="checkbox"/> Assignment Papers (cover sheet & document(s))9. <input type="checkbox"/> 37 CFR 3.73(b) Statement (when there is an assignee) <input type="checkbox"/> Power of Attorney10. <input type="checkbox"/> English Translation Document (if applicable)11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations12. <input type="checkbox"/> Preliminary Amendment13. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (Should be specifically itemized)14. <input checked="" type="checkbox"/> Small Entity <input type="checkbox"/> Statement filed in prior application Statement(s) <input type="checkbox"/> Status still proper and desired15. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed)16. <input type="checkbox"/> Other:
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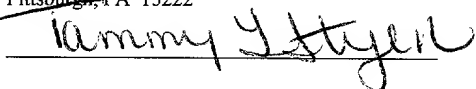
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PATENT

Attorney Docket No.: 000265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: L. Richard Carley)
)
Serial No.: Not Yet Assigned)
)
Filed: May 30, 2000)

Mark: MANUFACTURE OF MEMS STRUCTURES IN SEALED CAVITY USING
DRY-RELEASE MEMS DEVICE ENCAPSULATION

TRANSMITTAL FOR PATENT APPLICATION

Assistant Commissioner for Patents
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Dear Sir:

Enclosed for filing is the above-referenced application. Transmitted herewith for filing
are:

1. Utility Patent Application Transmittal (Form PTO/SB/05);
2. Fee Transmittal (Form PTO/SB/17);
3. Twelve (12) pages of specifications including twenty (20) claims;
4. Ten sheets of figures including nine (9) informal drawings;
5. Verified Statement Claiming Small Entity Statement; and
6. Declaration for Patent Application;

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
0	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

Respectfully submitted,

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**STATEMENT CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) & 1.27(b))--INDEPENDENT INVENTOR**

Docket Number (Optional)

000265

Applicant, Patentee, or Identifier: L. Richard Carley

Application or Patent No.: _____

Filed or Issued: _____

Title: DRY-RELEASE MEMS DEVICE ENCAPSULATION

As a below named inventor, I hereby state that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees to the Patent and Trademark Office described in:

- ☒ the specification filed herewith with title as listed above.
☐ the application identified above.
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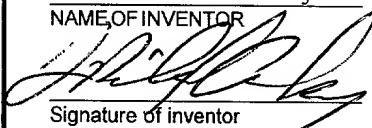
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I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

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Signature of inventor

Signature of inventor

Signature of inventor

5/19/2000
Date

Date

Date

MANUFACTURE OF MEMS STRUCTURES IN SEALED CAVITY USING DRY-RELEASE MEMS DEVICE ENCAPSULATION

Field of the Invention

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This invention relates to the process of manufacturing micro-electro-mechanical systems (MEMS), and, in particular, to MEMS devices having sealed cavities or encapsulated movable parts.

Background of the Invention

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This disclosure builds on prior art describing a method for constructing micro-electro-mechanical systems by using multiple sacrificial thin film layers removed using a liquid etch.

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It is known in the prior art to create sealed cavities on an integrated circuit for a variety of applications, for example, as a speaker or microphone. It is also known to encapsulate movable mechanical components on an integrated circuit within a sealed cavity. The encapsulation of micro-electro-mechanical structures in a sealed cavity is desirable for several reasons. First, the tolerance of the structures to ambient conditions, such as high humidity, is greatly improved. Second, the dicing and packaging of the MEMS devices is greatly facilitated. Third, when the cavity is at a low or very low ambient pressure, the Brownian noise due to the motion of gas molecules can be significantly reduced.

20

Processes to create sealed cavities on the surface of a silicon wafer using only thin film deposition and etching techniques have already been developed to create MEMS microphones and speakers for sound and ultrasound. Starting with a silicon substrate, which could in principle have CMOS devices and interconnects already patterned onto it, a protective layer is placed over the entire wafer. Next a sacrificial layer is deposited. Then, the sacrificial layer is patterned to remove it over all parts of the wafer that are not going to be microencapsulated. Next, an encapsulating layer is deposited over the entire wafer. Very small holes are then patterned and etched through the encapsulating layer at selected positions over the sacrificial layer, and the wafer is immersed in a liquid

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chemical bath containing an etchant that is highly selective, to dissolve the sacrificial layer while not attacking the encapsulating layer or the protective layer. Finally, an insulating or conducting layer that will act to seal the membrane must be deposited onto the wafer. The etch access hole can be sealed off either by material accumulating up
5 from or by material depositing laterally on the sides of the hole growing inward and sealing off the hole. In either case, the final layer serves to both plug the etch holes and to seal the cavity created when the sacrificial material was etched away.

It is also known to create MEMS microstructures within sealed cavities such as the one described above. See, for example, U.S. Patent Nos. 5,285,131 and 5,493,177
10 (both to Muller, et al.) in which methods to create an incandescent lamp and a vacuum tube respectively are disclosed. The method disclosed is as follows. A silicon substrate is covered with a non-etchable protective layer that is selectively removed, thereby exposing the silicon wafer in the region to be encapsulated. Then, a layer of poly-silicon is deposited and patterned to cover the exposed silicon window and extending up onto the
15 silicon nitride protection layer in selected positions that will be used as entry points for the liquid etching agents. Non-etchable conductors are then deposited and patterned on top of both the non-etchable mask layer and on top of the silicon substrate in the window. Next, a sacrificial layer is deposited and etched so that it only covers the structures in the region to be encapsulated. The encapsulation process proceeds with the deposition of an
20 encapsulation layer and the etching of small holes in the encapsulation layer located over the poly-silicon above the protection layer that will guide the etching agents into the cavity. In this case, the etching step requires two different liquid etchants - the first one to selectively etch away silicon and poly-silicon and a second one to etch away the sacrificial layer. The encapsulation process is completed by depositing a seal layer to seal
25 up the etch entry holes in the diaphragms.

It is desirable to use a wet etchant, in many cases hydro-fluoric acid, because of its high degree of selectivity, that is, the ability to selectively etch away the sacrificial layers, leaving behind the microstructure and cavity walls. However, one unfortunate problem when working with a "wet" etchant is that the surface tension generated as the
30 liquid evaporates can be strong enough to bend or even break delicate MEMS microstructures. Therefore, the use of liquid etching agents severely limits the

complexity of the MEMS microstructures that can be released from sacrificial layers in the cavity because only simple MEMS microstructures can tolerate the surface tension forces exerted by typical liquid etching agents as the surface is drying. MEMS devices having suspended structures have been developed using a wet release etch. However, the structures were quite simple, for example, wires supported at both ends with a small number of meanders. However, in order to create a wide range of MEMS devices, for example, acceleration sensors, quite flexible MEMS structures are necessary. These flexible structures would most likely be destroyed by the surface tension effects of a wet etch.

It is therefore desirable to create MEMS structures with out the wet etch step, eliminating potential damage to delicate MEMS structures by the surface tension created by the wet etchant. Such a process would improve yields of the devices, thereby making their production more economical. Additionally, more complex structures could be developed.

Summary of the Invention

The disclosed invention specifies improvements to the known MEMS fabrication process by selecting a combination of layers for the MEMS structural layers, seal layer and the sacrificial layers that allows release of the microstructures using a dry plasma etchant. Ideally, the dry etchant would have a high etch rate for the material composing the sacrificial layers and a low etch rate with respect to the material composing the structural and seal layers. This eliminates the undesirable liquid surface tension inherent in the wet etch process.

Description of the Drawings

Figures 1a and b show a top view and a side cross-sectional view respectively of the silicon CMOS wafer used as the base of the MEMS micro-encapsulated structure.

Figures 2a and b show a top view and a cross-sectional view respectively of the wafer of the Figure 1A with a sacrificial layer deposited thereon.

Figures 3a and b show a top view and a cross-sectional view respectively of the wafer of Figures 2a and b having a structural layer added thereon.

Figures 4a and b show a top view and a cross-sectional view respectively of the wafer of Figures 3a and b having a second sacrificial layer deposited thereon.

5 Figures 5a and b show a top view and a cross-sectional view respectively of the wafer of Figures 4a and b having a seal layer applied thereon.

Figures 6a and b show a top view and a cross-sectional view respectively of the wafer of Figures 5a and b having etch holes drawn therein.

10 Figures 7, 7a and 7b show a top view, a cross-sectional view along line 7b and a cross-sectional view along line 7c respectively of the wafer having the sacrificial layers removed by an etchant.

Figures 8a and 8b show a top view and a cross-sectional view respectively of the wafer having a second seal layer applied thereon, thereby sealing the etch holes.

15 Figure 9a and 9b show a top view and a cross-sectional view respectively of the second seal layer having been removed from the contact pad on the base wafer.

Detailed Description

20 In general, the invention disclosed refers to gas phase release of any number of microstructure layers whose movement is independent or coupled and which are encapsulated in the thin film seal layer. However, in order to explain the invention, one specific embodiment will be described in detail below, namely a microstructure that can be utilized as a Z-axis accelerometer. This device consists of a paddle shaped MEMS microstructure anchored at one point by a thin supporting member such that it can move
25 vertically within the sealed cavity.

30 Figure 1 illustrates the sequence of steps comprising the fabrication of the proposed encapsulated integrated microstructure CMOS process. We start by obtaining or fabricating a silicon CMOS wafer 2 coated with a layer of silicon nitride 4 and having metal pads interfacing to the original CMOS integrated circuit 6, 8 and 10 present as shown. Windows 7 and 9 appear in the silicon nitride layer 4 to allow access to metal pads 6 and 8. In the preferred embodiment, the metal pads would be aluminum, but may

alternatively be copper or any other conductive material.

To begin the fabrication process, a sacrificial layer 12 is deposited on top of the passivation layer of the standard CMOS wafer 2, which in this case is silicon nitride layer 4. The MEMS device fabrication steps are all performed at low temperature on top of the complete CMOS wafer 2, leaving the circuitry therein undisturbed. Cuts in the passivation layer 4 are left during the CMOS IC design and sacrificial layer 12 is removed over these cuts if access to the metal contacts is desired. The exposed metal contacts 6 and 8 are then used to make connections between the MEMS microstructure and the CMOS circuitry in silicon CMOS wafer 2 below. This is illustrated in Figure 2a.

In the preferred embodiment the microstructure may be composed of any metal, for example, Al, W, Ti, Ta, Cu, Ni, Mo, etc., but in the preferred embodiment would be made of aluminum. The selection of material for a particular microstructure layer is dictated by two factors. First, how much residual stress gradient in the material is acceptable for a particular application and, second, by the availability of a selective etchant that removes the portions of the microstructure which are undesired, while stopping on or having a low etch rate for the silicon nitride passivation layer 4 and the sacrificial layer 12.

The deposition of the MEMS layer is shown in Fig. 3a and in top view in Fig 3b. MEMS microstructure 14 is deposited by methods known by those with ordinary skill in the art and the undesirable portions are etched away, thereby leaving the desired shape of the microstructure behind. The top view of Figure 3b clearly shows the shape of the microstructure as being a paddle having a long thin beam attached to an anchor point, which in this case is metal contact 8.

Next, as shown in Fig. 4a, and in top view in Fig. 4b, a second sacrificial layer 16 is deposited over the microstructure. It can be seen from the top view that portions of the top sacrificial layer 16 will come into contact with portions of the bottom sacrificial layer 12, in particular, those areas near the edges of the paddle-shaped main body of the microstructure and those areas on either side of the thin connecting beam portion of the microstructure.

In the preferred embodiment, and, if possible as dictated by the shape of microstructure 14, sacrificial layers 12 and 16 will be of the same material and will have

a connection to each other, such that when etchant is introduced, both layers will be etched away without the need to etch additional etchant entry holes. Alternatively, sacrificial layers 12 and 16 may be of different materials. Although not necessary in the construction of the microstructure of this example, more complex microstructures, or multiple microstructures in the same cavity may require etching away of various sacrificial layers at different times, making it necessary to use different materials for the sacrificial layers and different etchants.

The preferred material for sacrificial layers 12 and 14 is photoresist. Photoresist is chosen for this reason because it can be etched with an oxygen plasma gas, which is not destructive of aluminum microstructure 14, silicon nitride passivation layer 4 or seal layer 18. Figures 4a and 4b show the deposition of second sacrificial layer 16.

If sacrificial layers 12 and 16 are of different materials it is possible to etch them separately by selecting an etchant that is selective to one and not the other. It is even possible that a wet etch could be used with one of the sacrificial layers. For example, sacrificial layer 16 may be phosphorous-doped glass and the etchant may be hydrofluoric acid. This may be desirable because the wet etchants are generally faster acting than the dry etchants. As long as the microstructure is held in place by one or more other sacrificial layers, the surface tension problem will be avoided. In this case, it is only necessary that the last sacrificial layer binding the microstructure in place be removed using the dry-etchant process.

Figure 5a shows the deposition of seal layer 18. This layer may be composed of an insulator or a conductor, depending on the desired electrical operation of the microstructure. Additionally, the seal layer must have a low enough residual stress and must be thick enough that the membrane created by the seal layer 18 will not buckle after the sacrificial layers 12 and 16 have been removed. In the preferred embodiment, seal layer 18 is the same metal as was chosen for the microstructure layer 14, but in alternate embodiments may be made of any material resistant to the etchant chosen. In the event an insulating material is chosen for seal layer 18, it may be patterned and removed to give access to the non-MEMS parts of the integrated circuit, such as bond pads 6 and 8. If seal layer 18 is a conductor, it may be contacting one or both of bond pads 6 or 8.

Next, one or more etchant access holes 20, shown in Fig. 6a and 6b are etched

into seal layer 18 such that communication can be established with sacrificial layers 12 and 16. This etch is done by any means well known to anyone of ordinary skill in the art. Preferably etch holes 20 will be as far away as possible from the actual MEMS microstructure. Next, as shown in Figs. 7a, 7b and 7c, the etchant is introduced into holes 20 and sacrificial layers 12 and 16 are etched away, leaving void 22. Figure 7b shows a cross-sectional view of the device through the center, while Figure 7c shows a cross-sectional view through one of the etchant access holes. A dry plasma etchant is used to avoid problems created by the surface tension of a wet etchant. In the preferred embodiment, the etchant is oxygen plasma. Oxygen plasma was chosen because it is highly selective with respect to the etching sacrificial layers 12 and 14, which may be photoresist or other organic polymers, while having an extremely low etching rate for a wide variety of metals and insulators.

At this point, microstructure 14 is able to move vertically within the cavity created by open space 22 previously occupied by sacrificial layers 12 and 16, with beam 15 acting as a spring and contact pad 8 acting as an anchor point. Depending upon the distance from the etch access holes to the furthest point of sacrificial layers 12 or 16 to be removed, etching time using the oxygen plasma may be quite long. It is preferred for this reason that a barrel etcher be used in the etching process such that a plurality of wafers may be etched at the same time.

The final step, shown in Fig. 8a, is the application of a second seal layer 26 to seal etch holes 20. In the preferred embodiment, seal layer 26 is the same metal as seal layer 18 and MEMS microstructure 14. As shown in Fig. 9, if the second seal layer is not a conductor then it may be etched away using well known methods from the area over contact pad 6, or it may be left in electrical contact with contact pad 6., final seal layer 26 may be etched away from contact pad 6, or, if seal layer 26 is composed of a conductor, may be left in place.

A simple microstructure that could be utilized as a Z-axis accelerometer has been described to show the general process of creating a microstructure in a sealed cavity using a dry-etch process. However, as realized by one of ordinary skill in the art, and as contemplated by the scope of this patent, the process may be used to build microstructures of more complexity, involving many combinations of sacrificial and

I Claim:

1. A method of fabricating a sealed cavity for containing a microstructure comprising the steps of :
 - providing a substrate;
 - depositing one or more sacrificial layers on said substrate;
 - depositing a first seal layer on top of said one or more sacrificial layers;
 - forming one or more holes in said first seal layer, said holes communicating with said one or more sacrificial layers;
 - introducing an etchant into said one or more holes such that said one or more sacrificial layers are etched away by said etchant;
 - depositing a second seal layer on top of said first seal layer, said second seal layer sealing said one or more holes in said first seal layer;
 - wherein said etchant used to remove the last of said sacrificial layers is a non-liquid material.
2. The method of claim 1 further comprising the step of choosing said etchant to etch said sacrificial layers without substantially etching said substrate or said first seal layer.
3. The method of claim 1 wherein said substrate is a silicon wafer having a layer of silicon nitride deposited thereon.
4. The method of claim 2 wherein said etchant is oxygen plasma and said one or more sacrificial layers are composed of photoresist.
5. The method of claim 1 further comprising the step of forming a microstructure in said cavity.
6. The method of claim 5 wherein said step of forming a microstructure in said cavity comprises the steps of:

depositing one or more layers of structural material between said one or more sacrificial layers;
shaping said structural material to the shape of the desired microstructure; and
removing said one or more sacrificial layers using a non-liquid etchant.

7. The method of claim 6 wherein said step of shaping comprises the steps of:
depositing a layer of photoresist over said layer of structural material;
etching away said structural material leaving the desired shape; and
removing any remaining photoresist material.
8. The method of claim 6 wherein said structural material is resistant to said etchant.
9. The method of claim 8 wherein said structural material is aluminum and wherein said etchant is oxygen plasma.
10. The method of claim 9 wherein said sacrificial layers are composed of an organic polymer.
11. The method of claim 10 wherein said organic polymer is photoresist
12. The method of claim 7 wherein said microstructure is released when all surrounding sacrificial layers are etched away.
13. The method of claim 7 wherein said sacrificial layers are made of different materials and wherein a wet etchant may be used to remove all but the last or said sacrificial layers holding said microstructure.
14. The method of claim 13 wherein said last sacrificial layer holding said microstructure is removed using said non-liquid etchant.
15. The method of claim 6 wherein each all of said sacrificial layers are composed of the

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0	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

16. The method of claim 15 wherein said etchant is oxygen plasma and wherein each of said one or more sacrificial layers are composed of photoresist.
17. The method of claim 15 wherein said sacrificial layers may be composed of different materials.
18. The method of claim 17 wherein at least one of said sacrificial layers is composed of photoresist.
19. The method of claim 18 wherein said sacrificial layers composed of photoresist are removed last.
20. The method of claim 19 wherein said sacrificial layers composed of photoresist are removed using an oxygen plasma etchant.

ABSTRACT

The disclosed fabrication methodology addresses the problem of creating low-cost micro-electro-mechanical devices and systems, and, in particular, addresses the problem of delicate microstructures being damaged by the surface tension created as a wet etchant evaporates. This disclosure demonstrates a method for employing a dry plasma etch process to release encapsulated microelectromechanical components.

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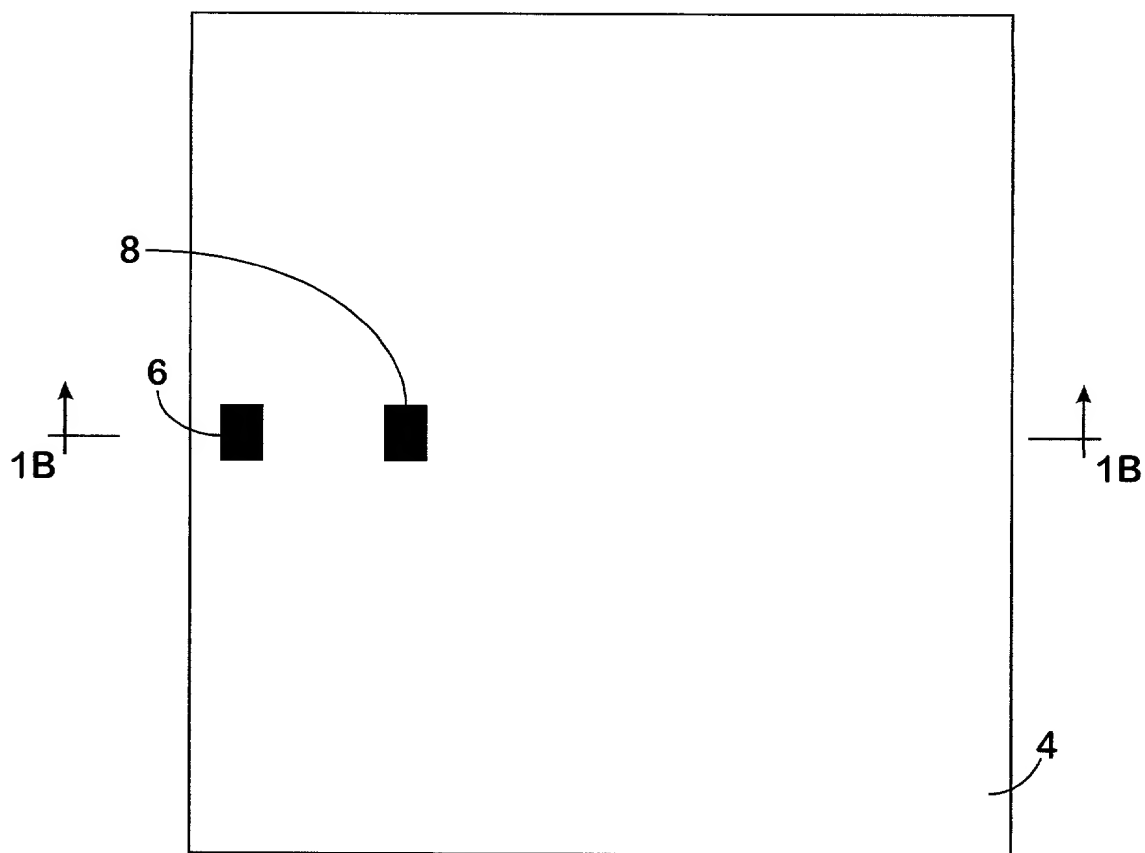


FIG. 1A

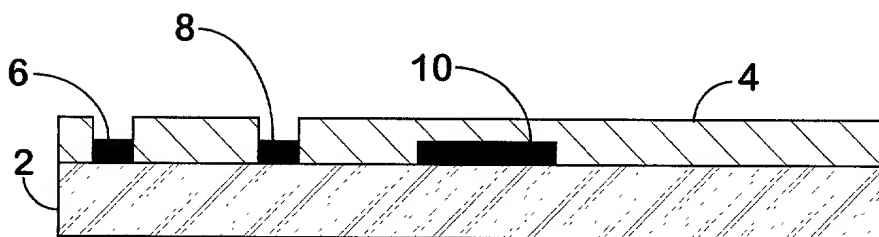


FIG. 1B

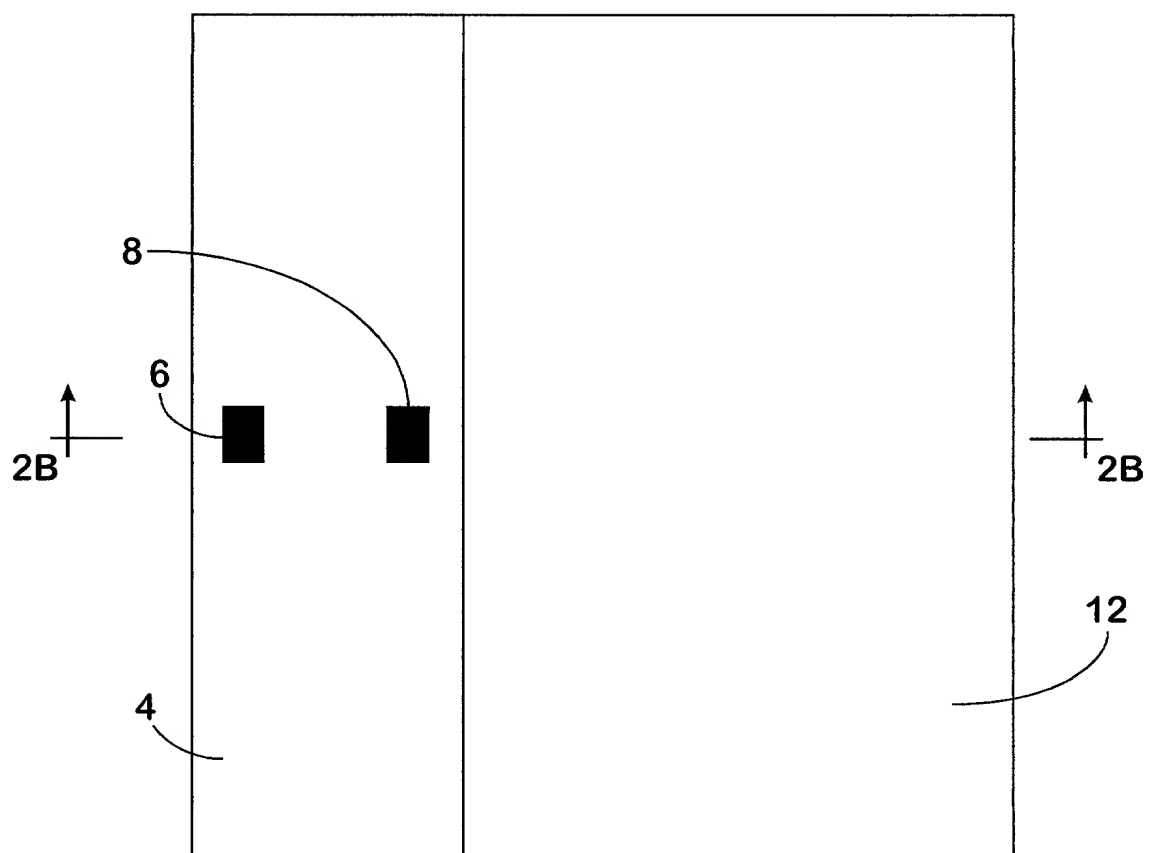


FIG. 2A

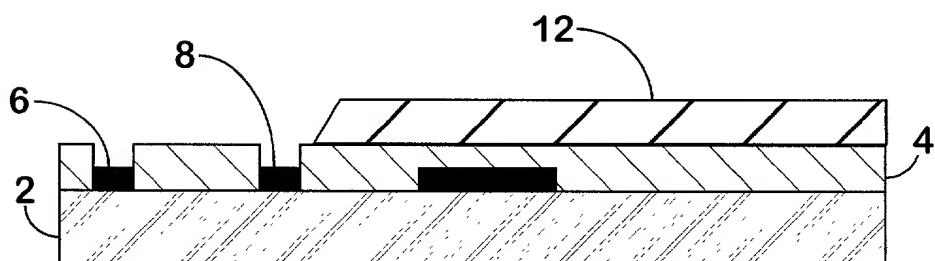


FIG. 2B

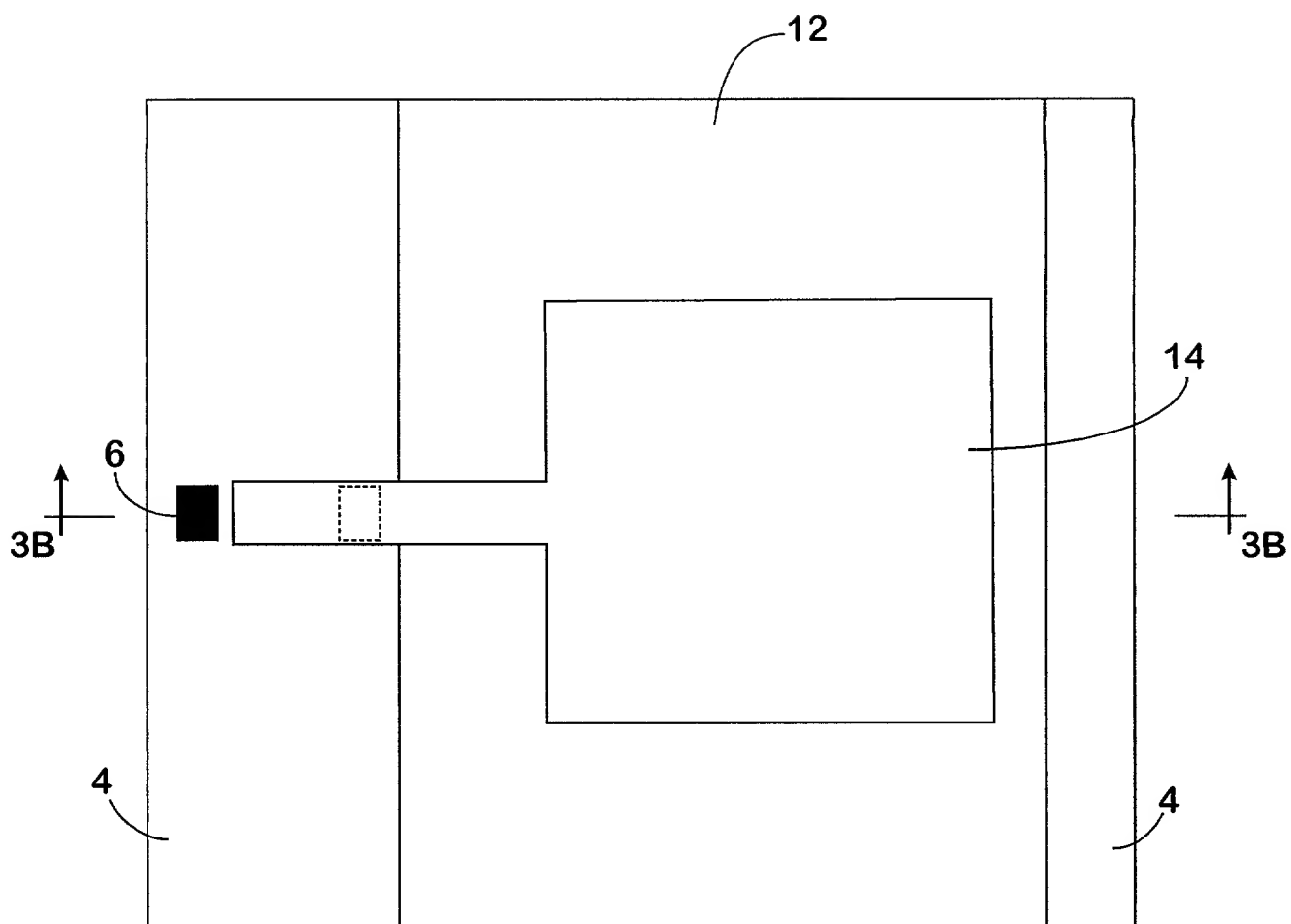


FIG. 3A

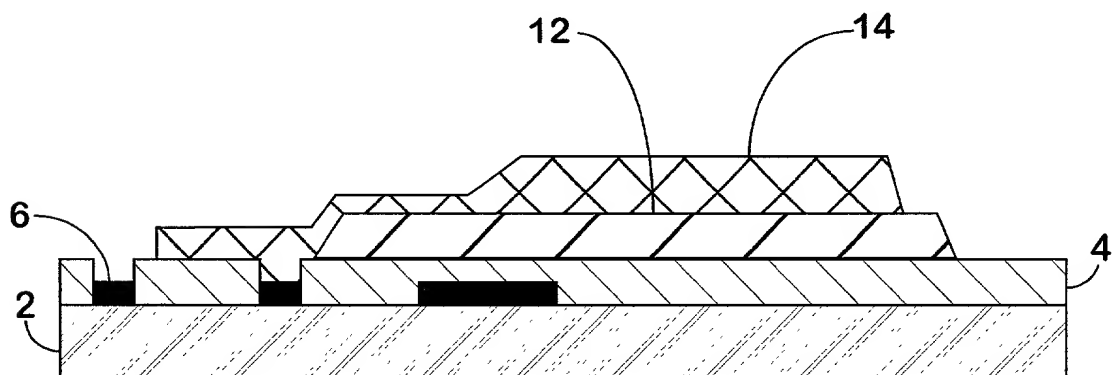


FIG. 3B

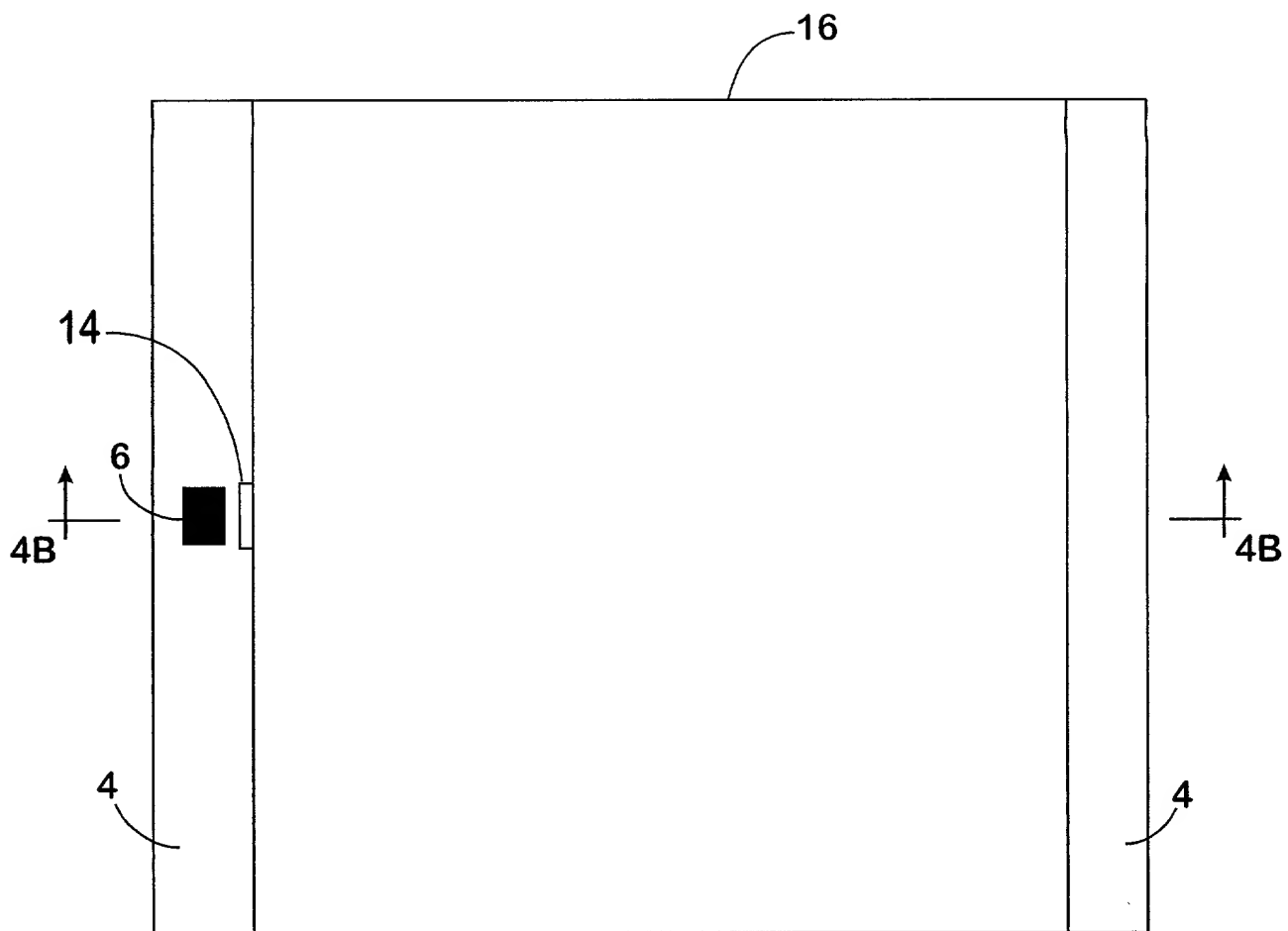


FIG. 4A

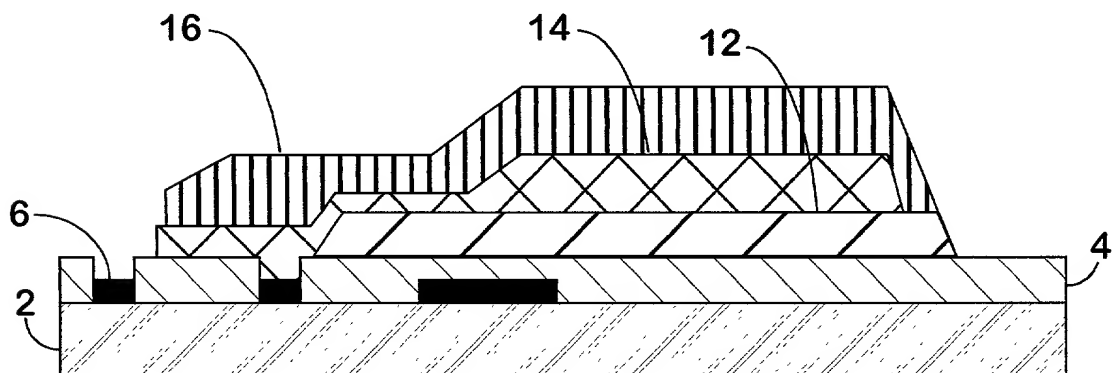


FIG. 4B

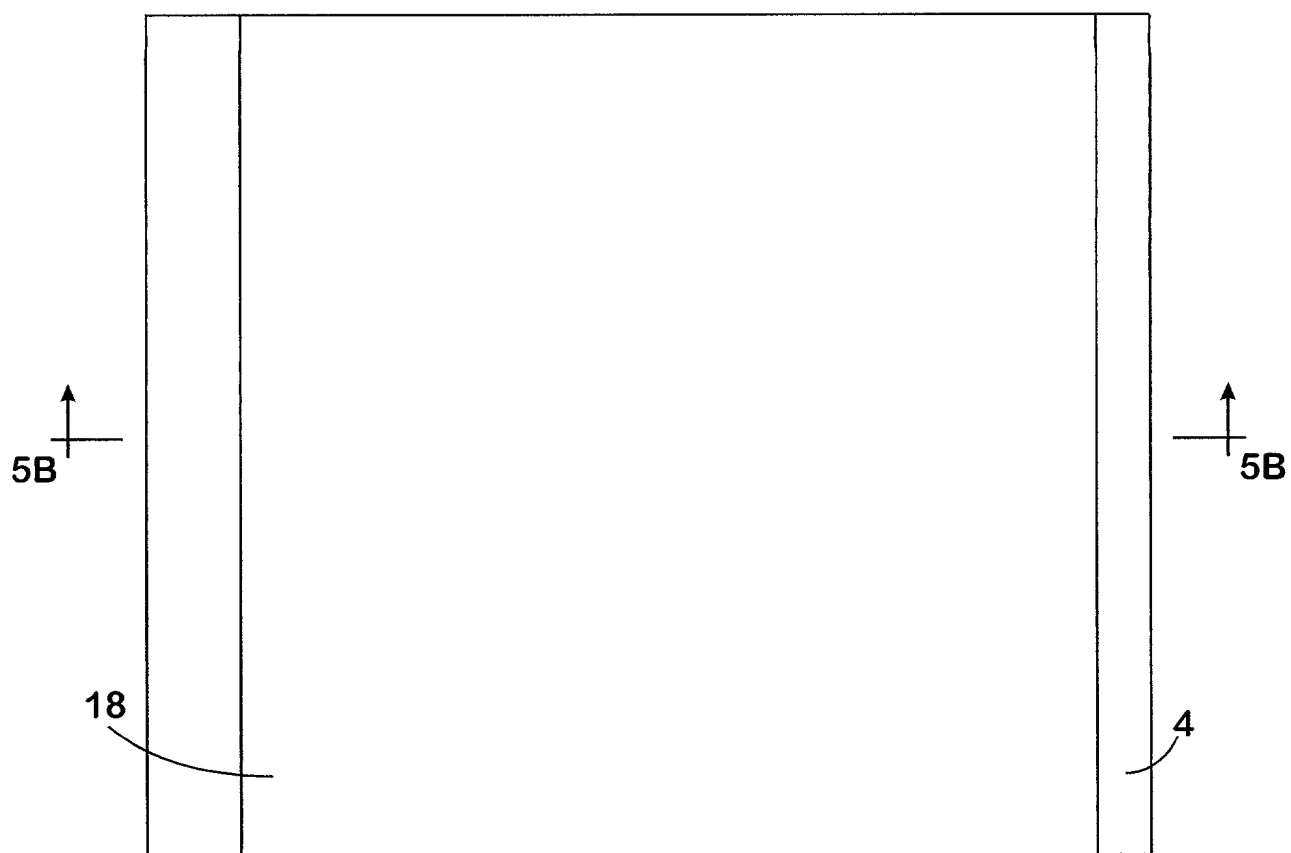


FIG. 5A

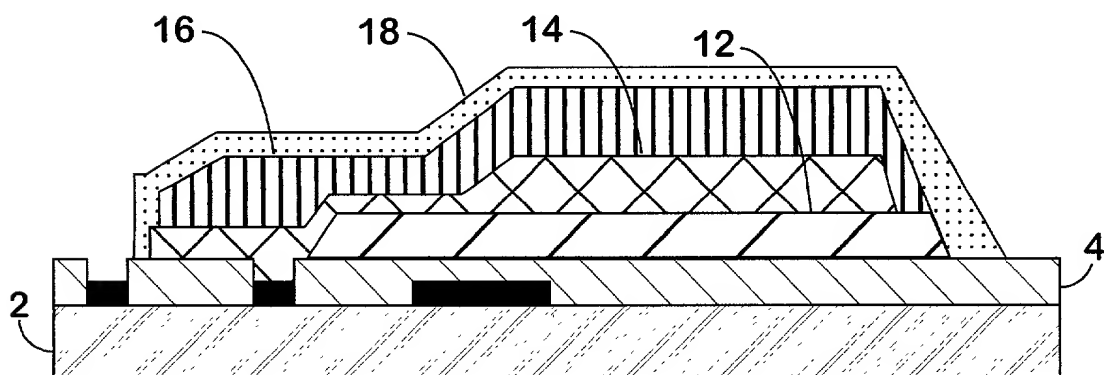


FIG. 5B

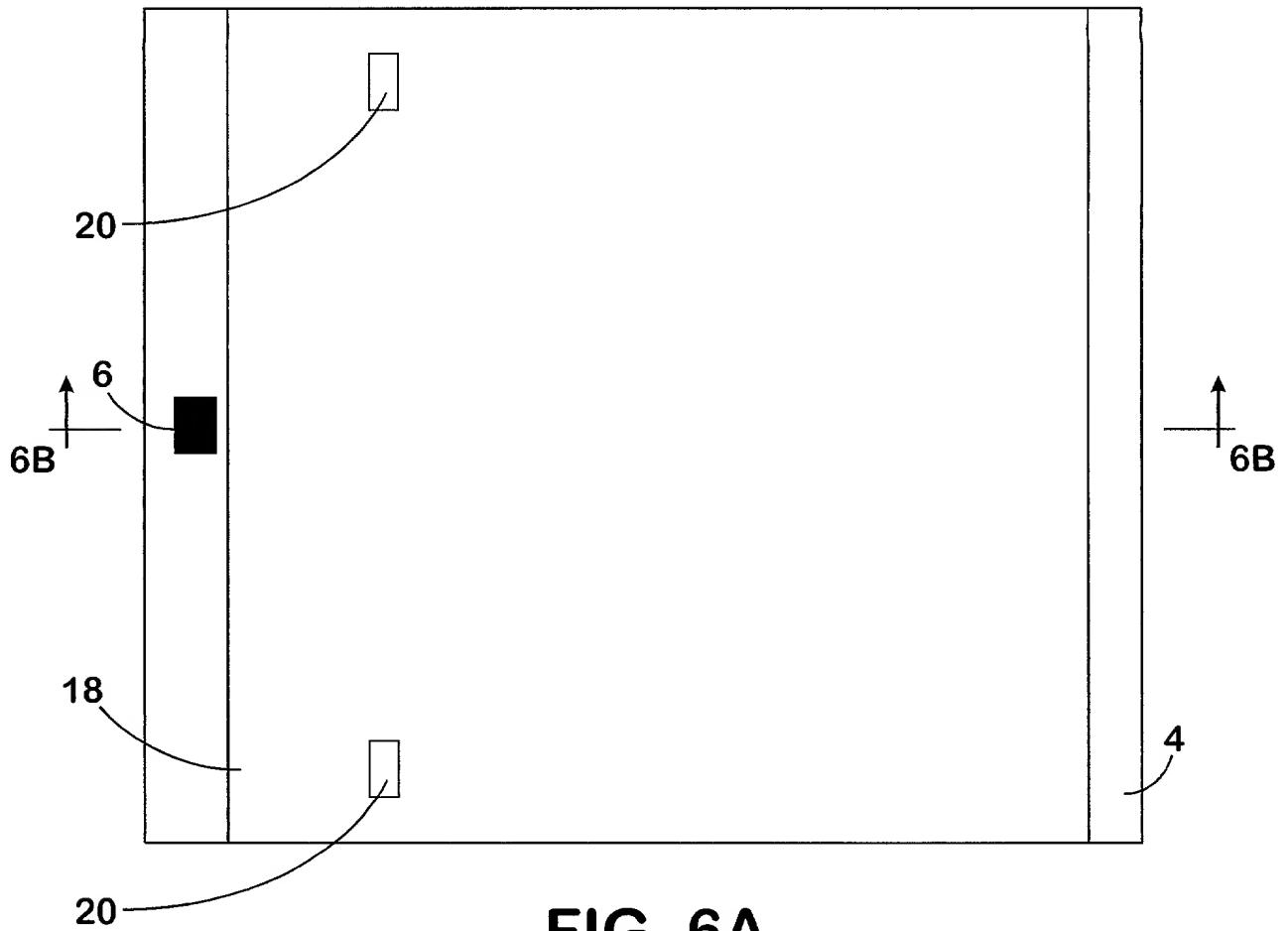


FIG. 6A

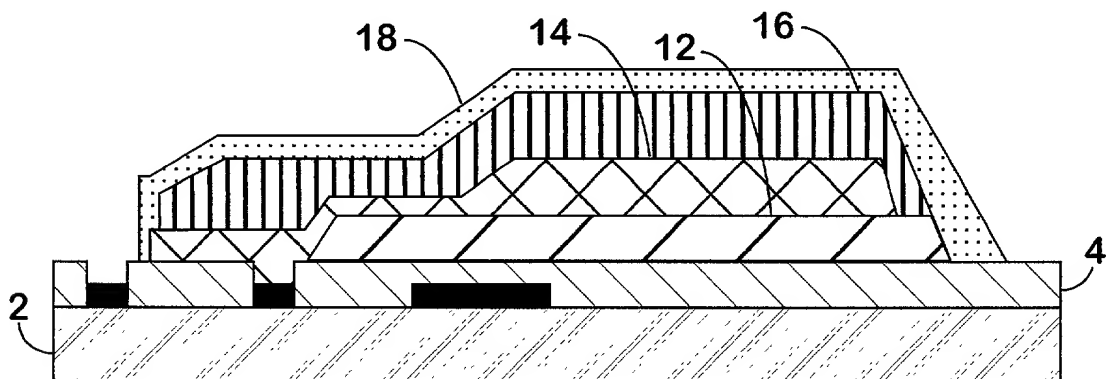
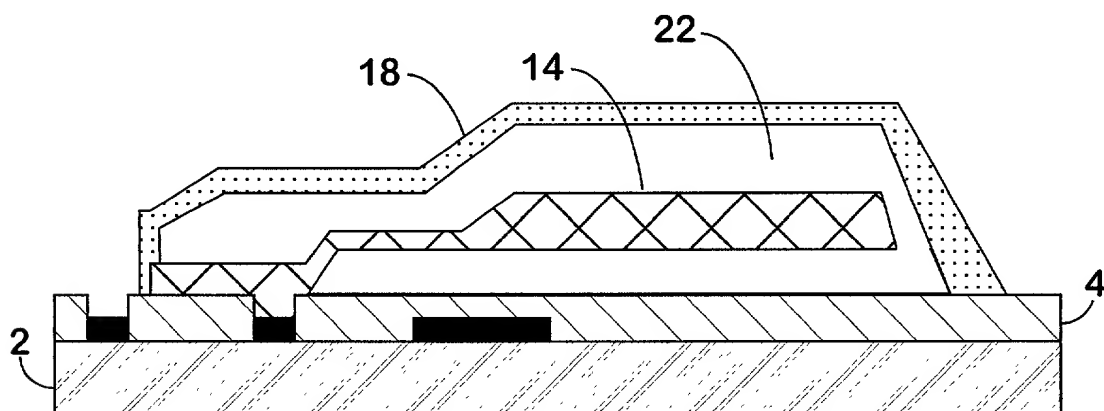
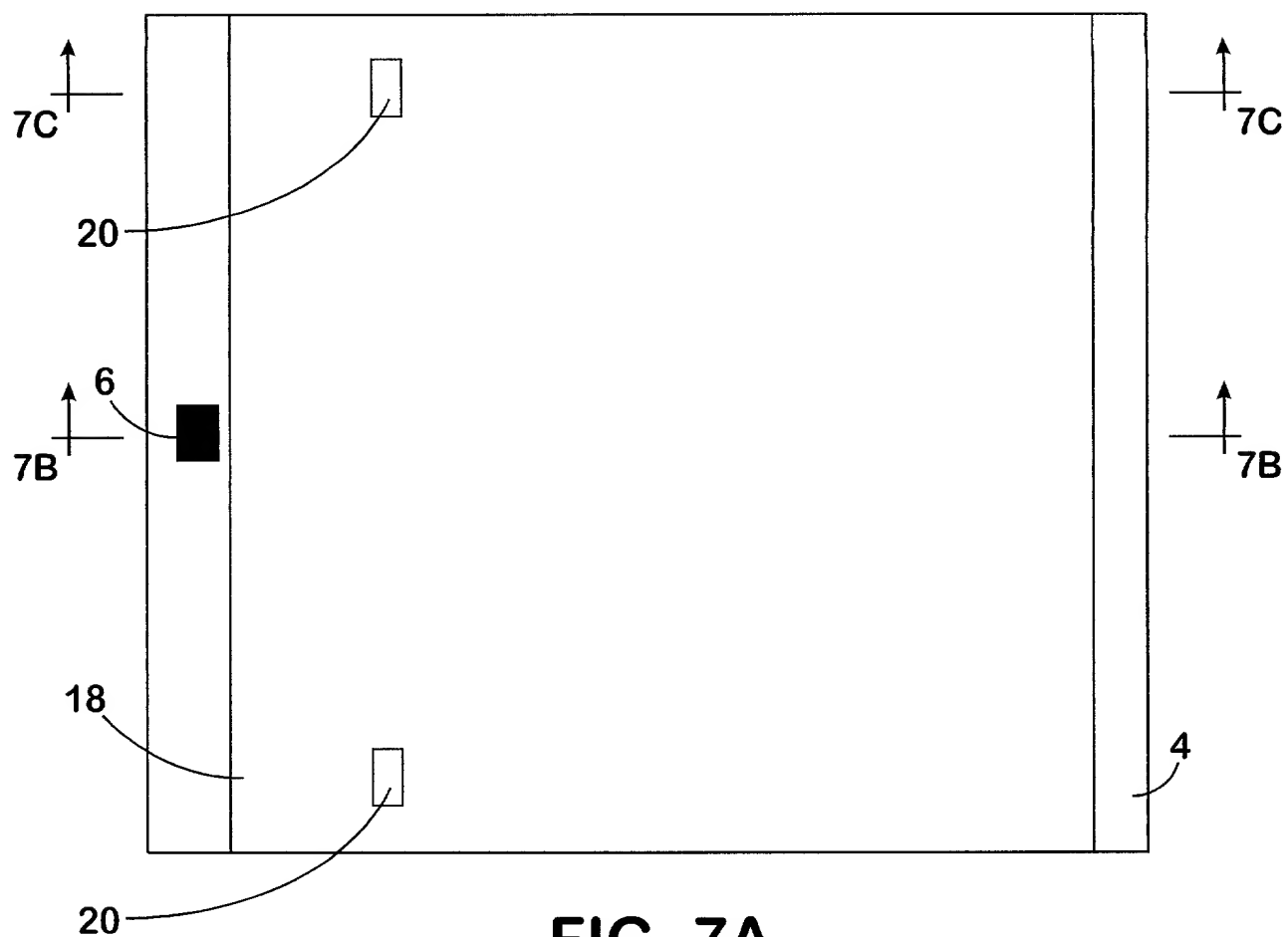


FIG. 6B



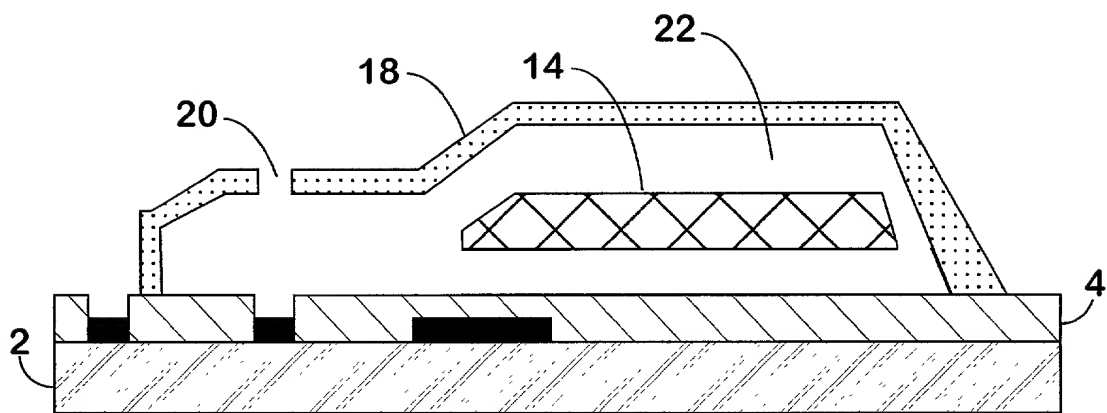


FIG. 7C

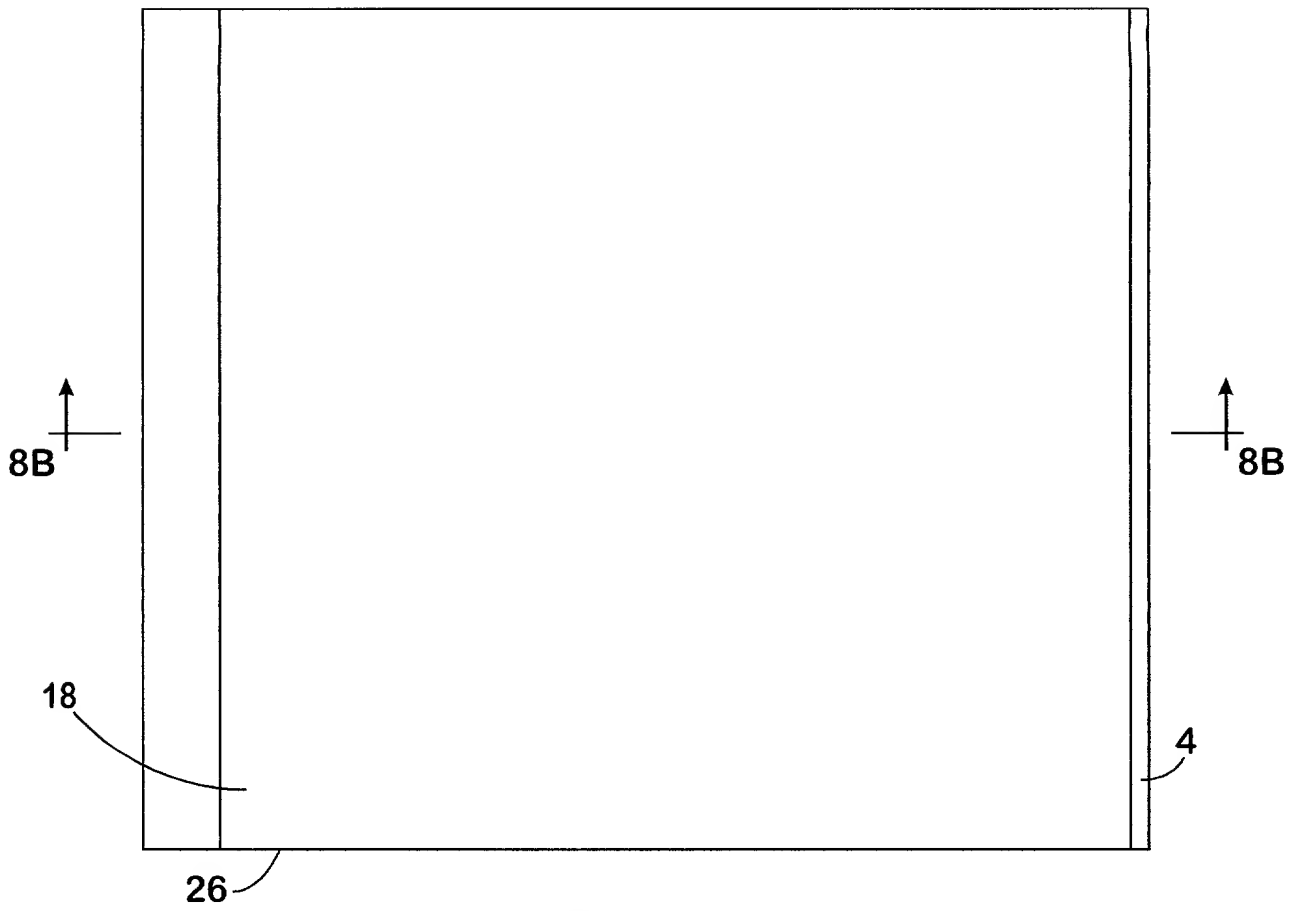


FIG. 8A

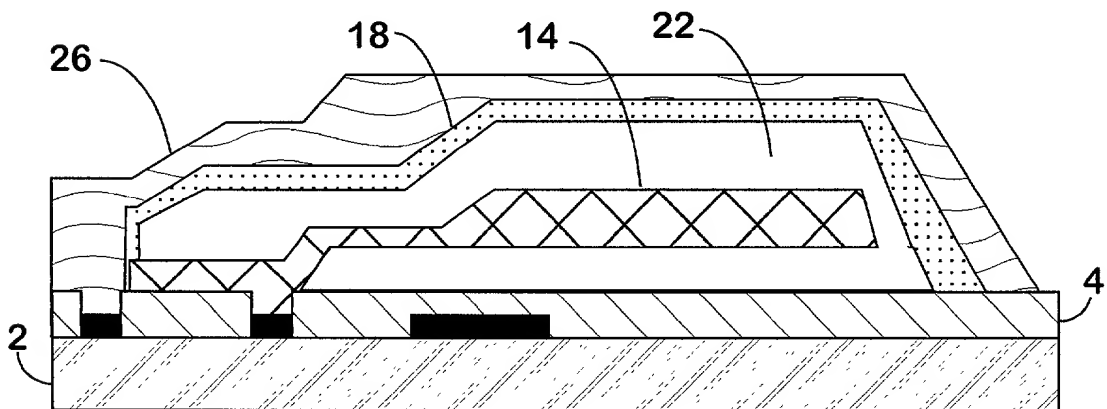


FIG. 8B

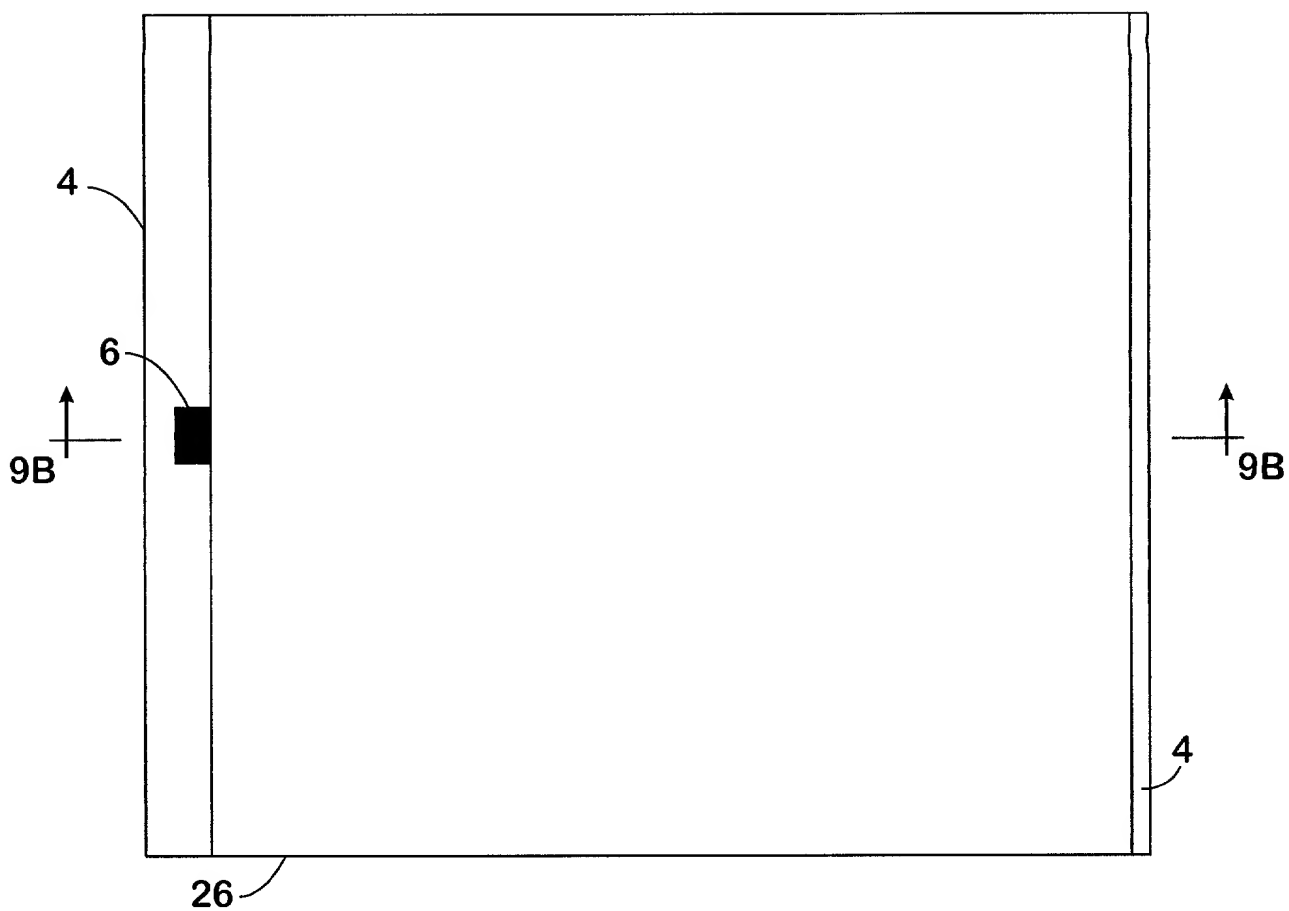


FIG. 9A

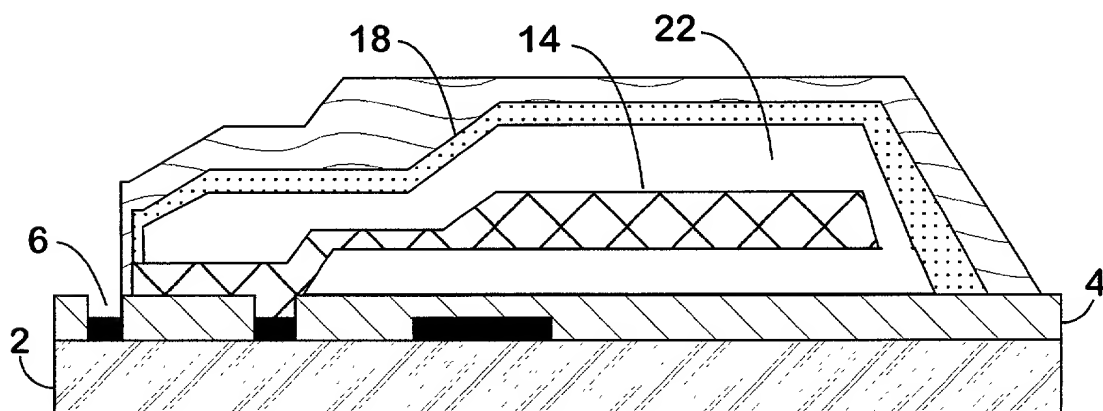


FIG. 9B

DECLARATION FOR PATENT APPLICATION

Docket No. 000193

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe that I am an original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled **DRY-RELEASE MEMS DEVICE ENCAPSULATION**, the specification of which is being filed concurrently herewith;

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims; and

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

POWER OF ATTORNEY

I hereby appoint the following attorney(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith: Lynn J. Alstadt, Reg. No. 29,362; George P. Baier, Reg. No. 26,717; Dennis M. Carleton, Reg. No. 49,938; Michael L. Dever, Reg. No. 32,216; John E. Grosselin, III, Reg. No. 38,478; Bryan H. Opalko, Reg. No. 40,751; Michael G. Panian, Reg. No. 32,623 and Carla J. Vrsansky, Reg. No. 36,958.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section

Post Office Address: Glen Mitchell Road, Sewickley, PA 15143

[illegible]